

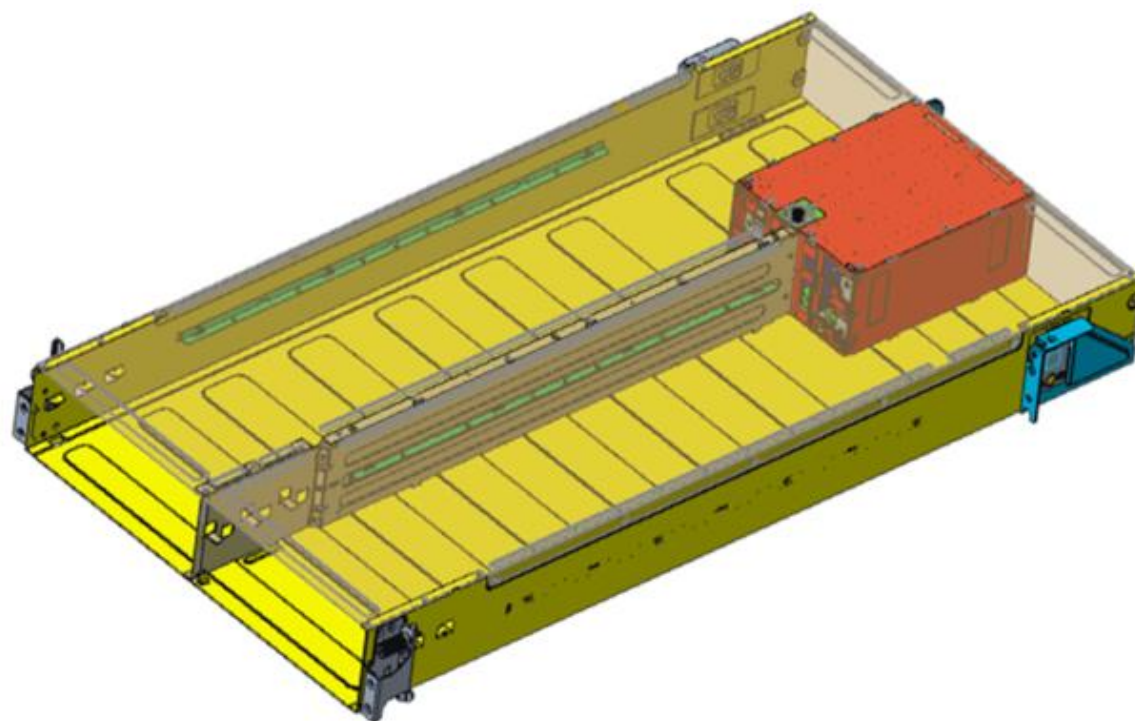
D3 chassis overview

Features, specifications, and management

Lenovo

Product overview

The ThinkSystem D3 chassis is the successor to the D2 chassis. It has a newly designed chassis skeleton which can support four 1U nodes or two 2U nodes in a single chassis. The D3 chassis supports Intel-based nodes (the SD530 V3 or SD550 V3) or AMD-based nodes (the SD535 V3). The D3 chassis has configurations with one to three air-cooled PSUs.



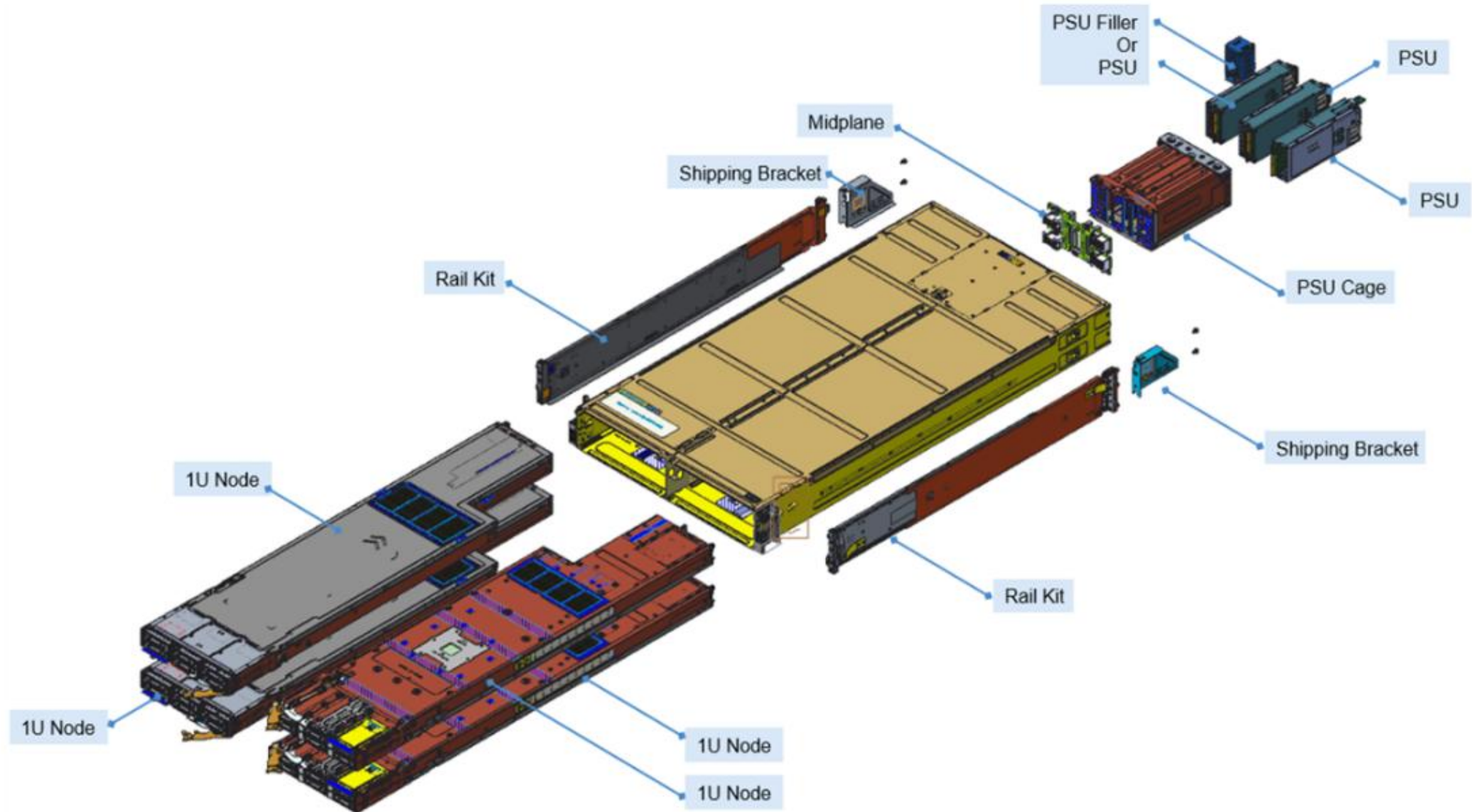
Node installation configurations

- The D3 chassis has four node installation configurations.
- At present, nodes installed in a chassis must belong to the same platform, either Intel or AMD.

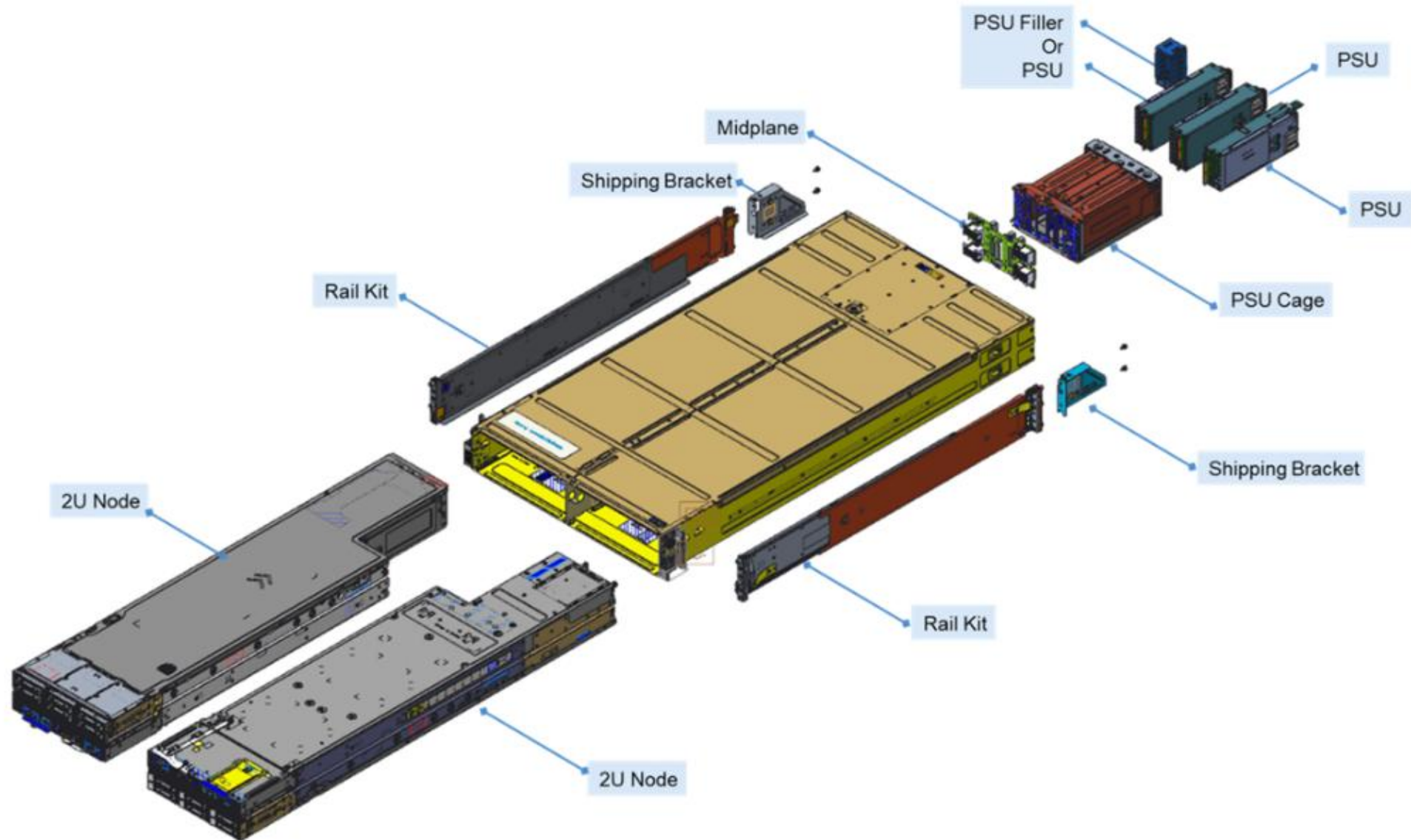
Type 1	1U (Node 3)	1U (Node 4)	Type 2	2U (Node 1)	1U (Node 4)
	1U (Node 1)	1U (Node 2)			1U (Node 2)
Type 3	1U (Node 3)	2U (Node 2)	Type 4	2U (Node 1)	2U (Node 2)
	1U (Node 1)				



D3 chassis with four 1U nodes – exploded view

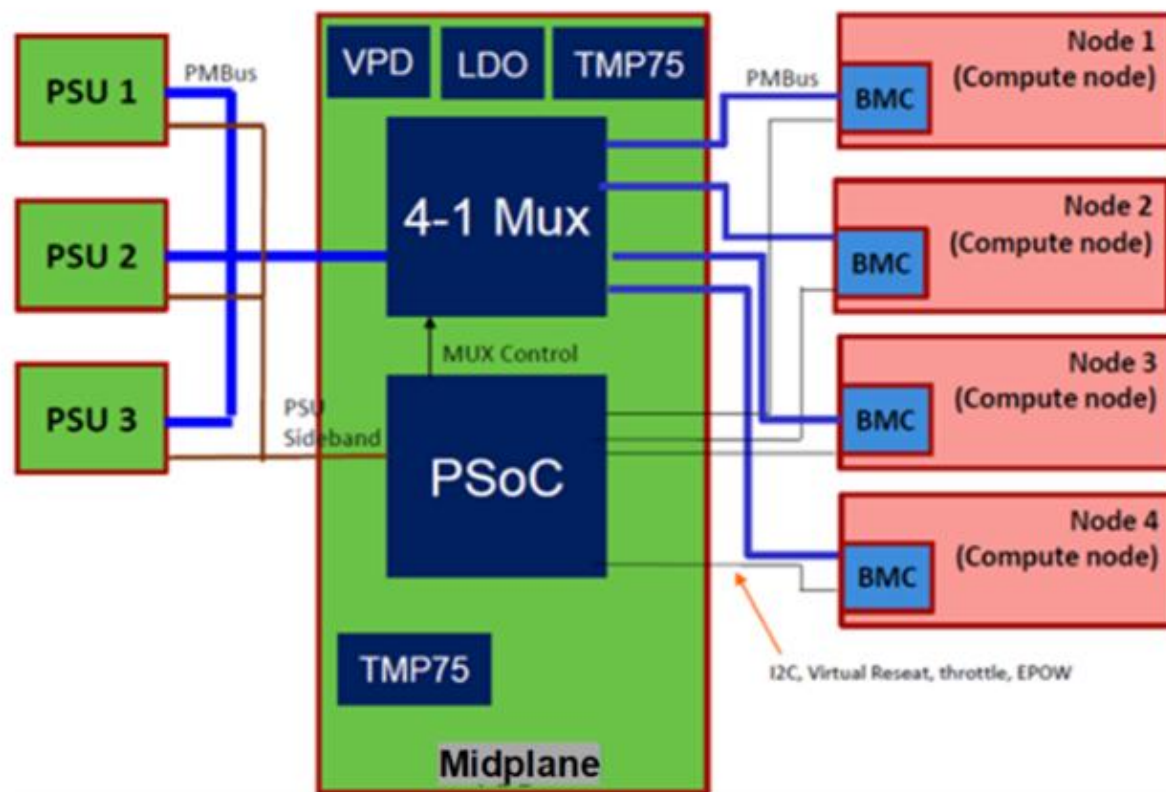


D3 chassis with two 2U nodes – exploded view



D3 chassis diagram

The D3 chassis has an SMM-free design. To manage or monitor the status of the D3 chassis, the end user must log in to the master node of XCC2 or use IPMI commands.

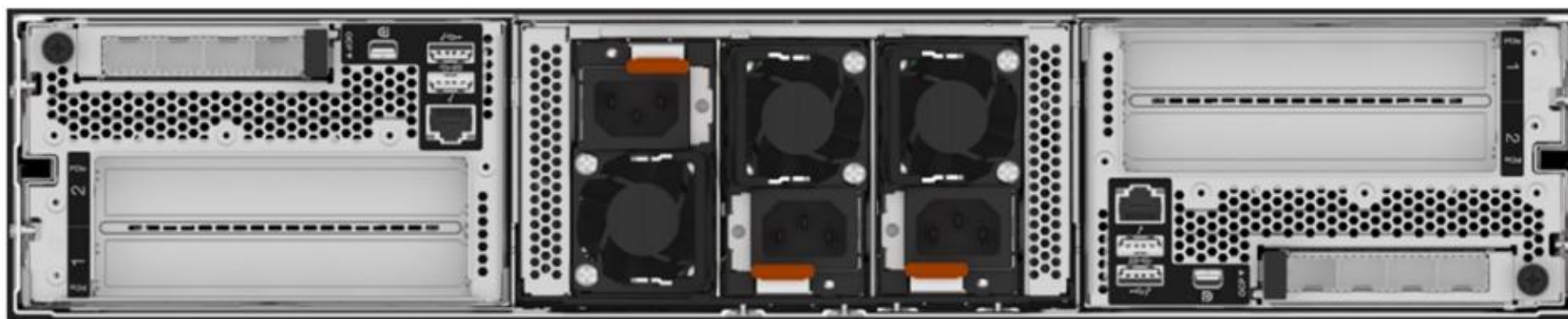


The midplane PSoC supports PSU and node-present detection

D3 chassis rear

The D3 chassis supports three hot-swap power supplies (PSU). Note the following rules when installing PSUs:

- PSUs must be installed in the following order: PSU1, PSU2, PSU3
- Installed PSUs must be of the same type and model
- Instructions on the [guiding label](#) in each slot must be followed – in slot 1, the PSU must be installed with the fan at the bottom, and in slots 2 and 3, the fan must be at the top

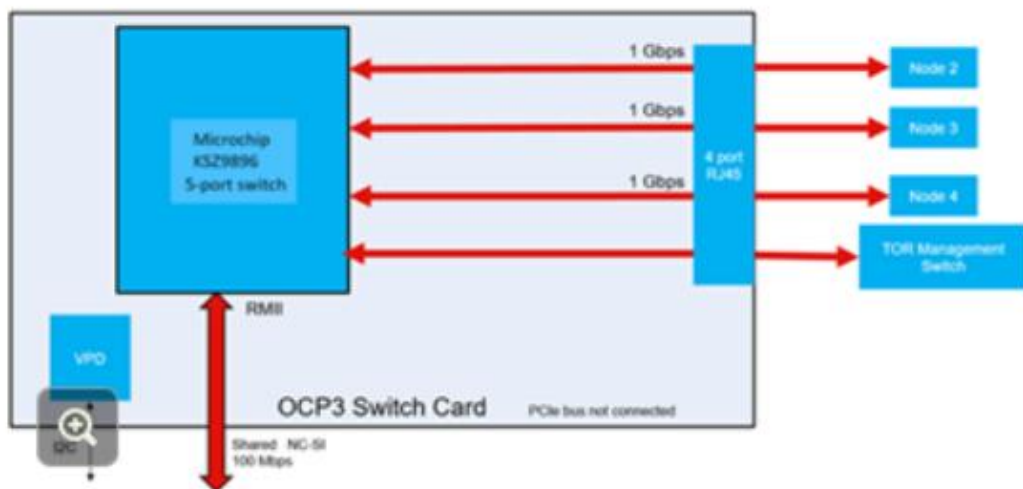


PSU1 PSU2 PSU3

Note: For more information about PSUs supported by the D3 chassis, refer to

Daisy chain topology

For system management, the SD535 V3 supports an OCP 3.0 SFF adapter in an OCP slot for daisy chain connections. With external cabling, it provides a 1 Gbps switch function between the nodes.



Node 1 is the master node by default.

Daisy-chain topology (2U4N)

