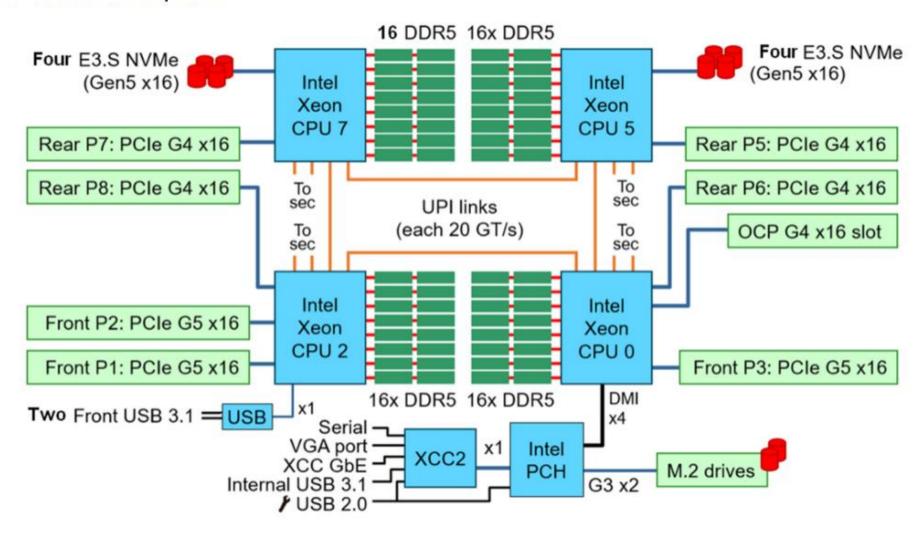
System configurations and diagrams

Primary chassis block diagram

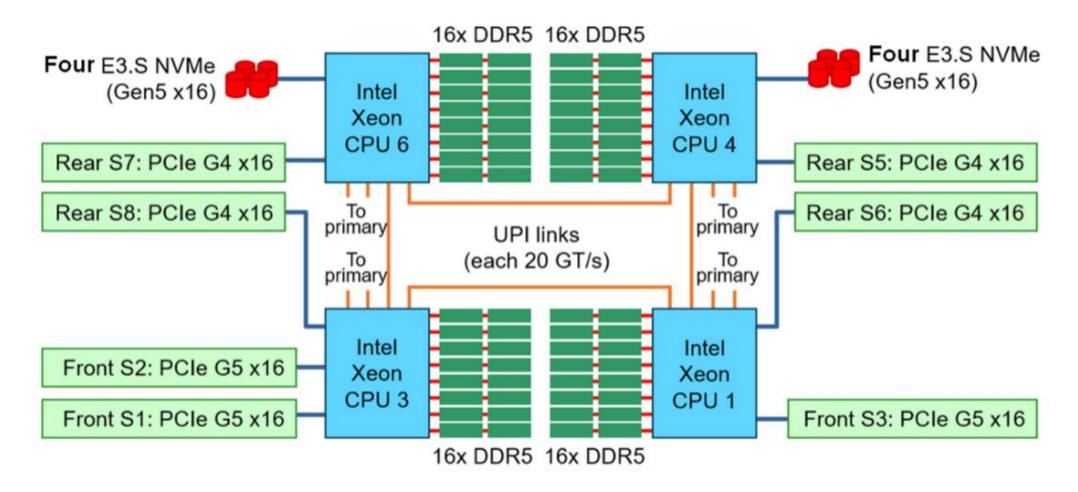
The primary chassis contains the Intel Platform Controller Hub (PCH), which is used to connect to the M.2 adapter and all the front ports.





Secondary chassis block diagram

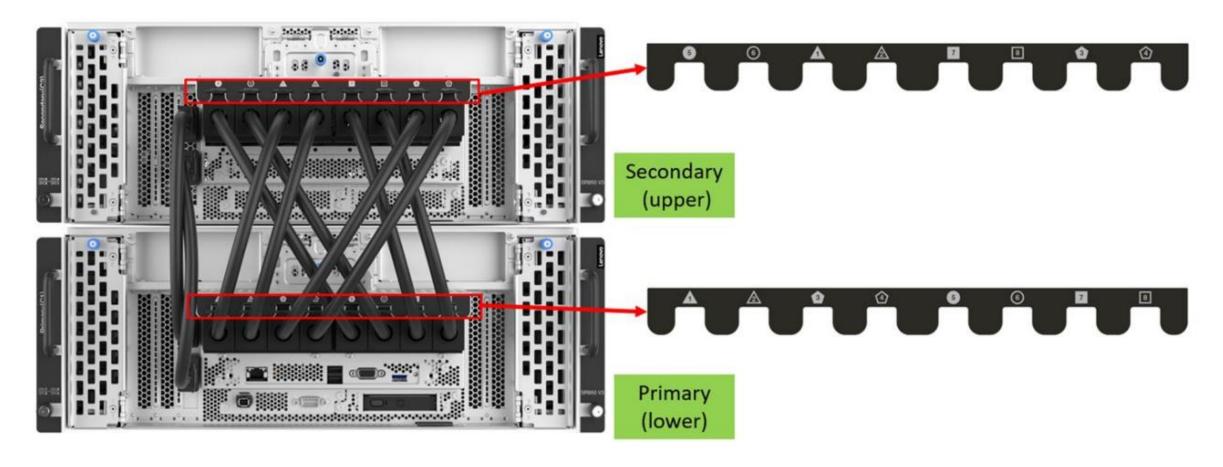
There is no XCC2, front USB, PCH, or M.2 drive in the secondary chassis.





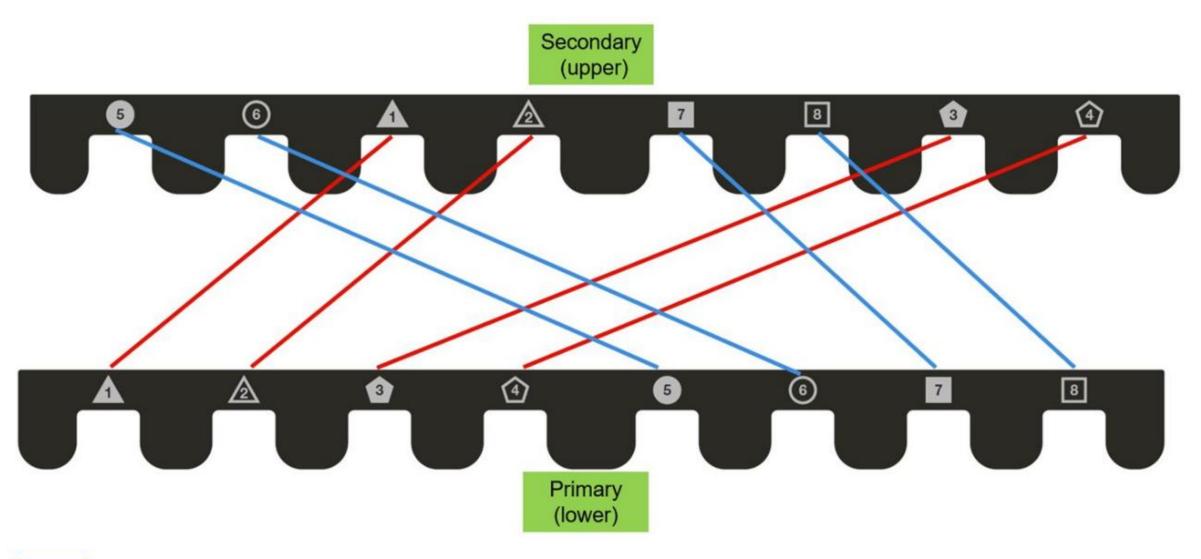
External cable routing – UPI cables

There are UPI connection labels on the top of the UPI assembly for both the primary and secondary servers. Follow the 1 to 1, 2 to 2, etc. rule to connect the UPI cables.



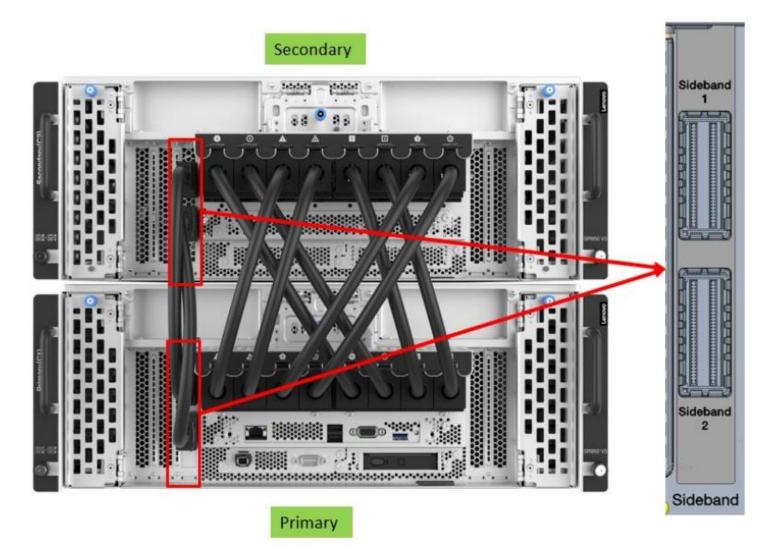


External UPI cable connection diagram





External sideband cable routing



 Connect the sideband 1 connector on the primary chassis to the sideband 1 connector on the secondary chassis.

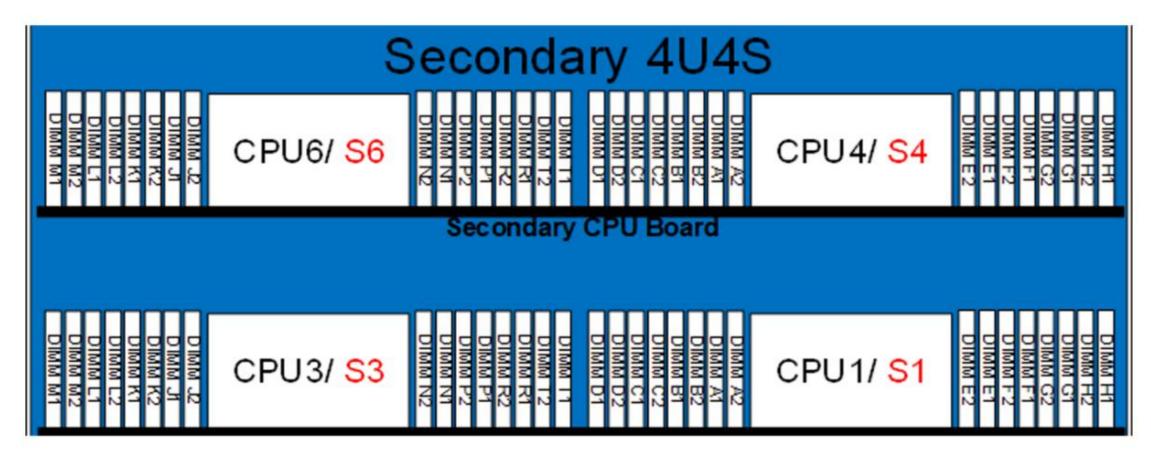


 Connect the sideband 2 connector on the primary chassis to the sideband 2 connector on the secondary chassis.





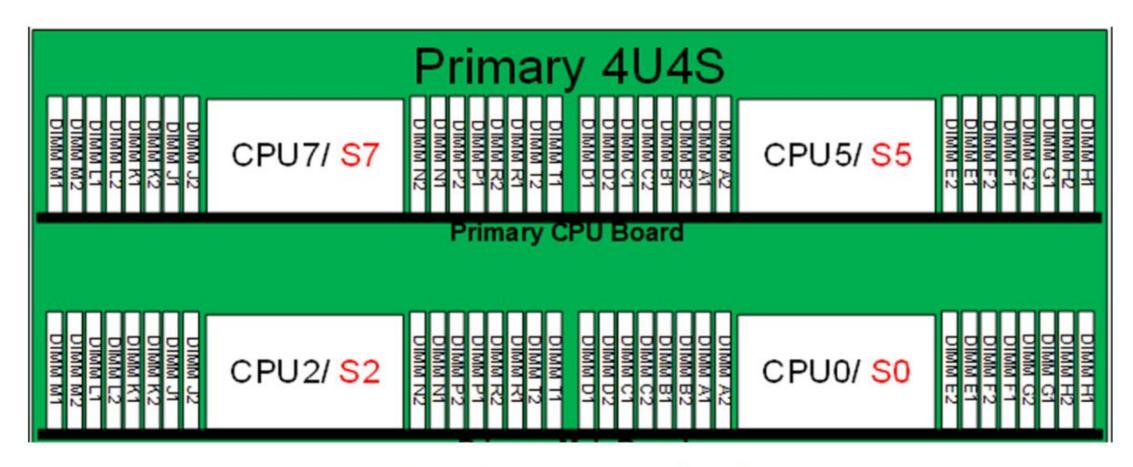
Secondary chassis processor and memory layout



Secondary lower processor board



Primary chassis processor and memory layout



Primary lower processor board



PCle riser card installation order

All the rear PCIe riser cards are optional and should be installed in the follow order:

- P7 and P8
- P5 and P6
- S7 and S8
- S5 and S6



