# XCC3 on ThinkSystem V4 servers

New features and enhancements

#### Overview

XCC3 features the following hardware and design changes:

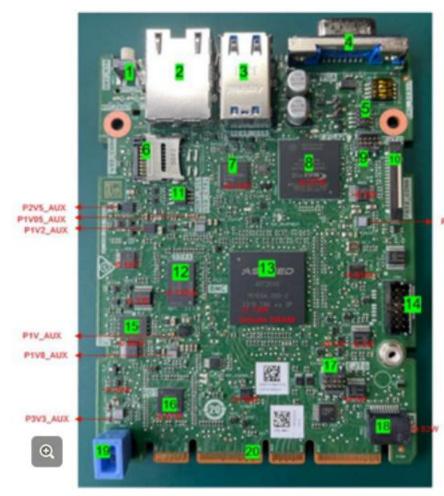
- XCC3 uses DC-SCM as the BMC hardware module
  - The SD520 V4 has an onboard BMC chip and uses an RoT card instead of a DC-SCM.
     BMC/UEFI flash chips and the PFR chip are on the RoT card
- A move to OpenBMC-based new architecture
  - Eliminates the dependency on Vertiv
  - Open source-based solution
  - In-house design with full control
  - Flexible architecture for future extensions
  - As far as possible, user interfaces are kept compatible with the previous generation
- A phased approach to deliver a full-function stack
  - Almost every single function is re-designed and re-coded
  - Schedule/resource constraints



#### DC-SCM card

The BMC chip was stored on the RoT module in V3 servers. Except with the SD520 V4, the BMC chip has been moved to the DC-SCM card in V4 servers.

- The new BMC hardware module follows OCP spec
- The BMC chip, BMC/UEFI flash, and PFR chip are all on one board
- New power sequence management through the SCM FPGA and HPM FPGA
- Two FPGAs (SCM FPGA and HPM FPGA), which the BMC needs to authenticate and update
- As with the ThinkSystem V3 platform, the AST2600 is used as the BMC chip
- New UEFI SPI flash is 64 MB (V3 is 32 MB)
  - Longer UEFI firmware update time

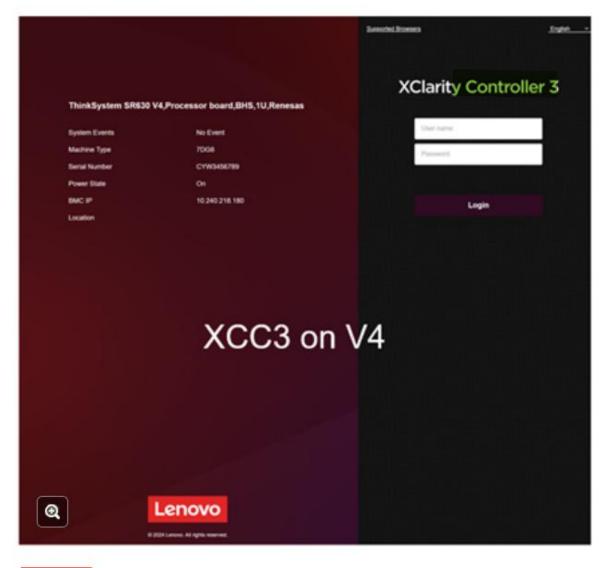


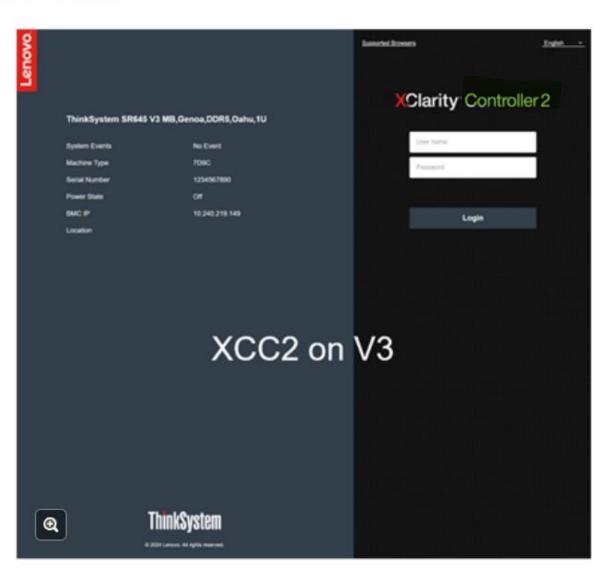
#### Key component list

- 1. NMI button w/ 3xLEDs
- 2. RJ45 connector
- 3. 2xUSB 3.0 connector
- 4. VGA port
- 5. BMC/ uEFI debug neader
- 6. Micro SD socket
- 7. USB Host controller
- 8. SCM FPGA Intel MAX10
- PSV\_AUX 9. FPGA JTAG Header
  - 10. PHY2 connector
  - 11. USB ROM
  - 12. DDR4 512M
  - 13. Aspeed AST2600 BMC
  - 14. COM port/FW programming header
  - 15. uEFI ROM(WSON8)
  - 16. PFR Chip
  - 17. BMC JATG Header
  - 18. PRC TPM/BMC TPM Option card
  - 19. Internal Handle
  - 20. DC-SCI (168pin Gold finger)



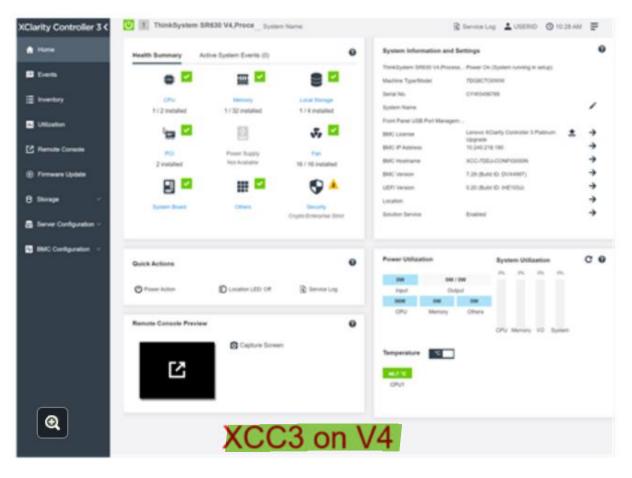
# XCC3 and XCC2 login page comparison

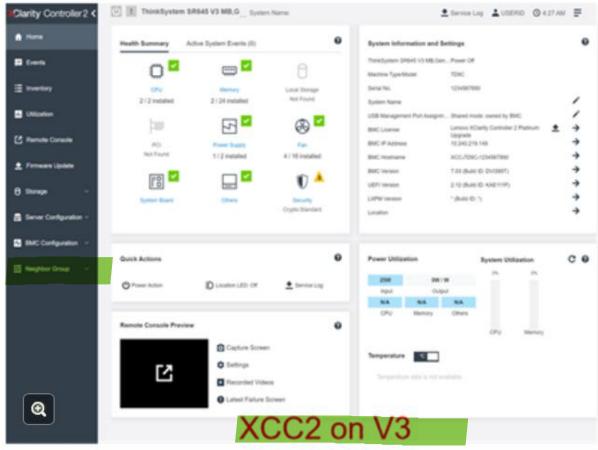






# XCC3 and XCC2 home page comparison







### BMC configuration settings through OneCLI

- BMC and UEFI settings both leverage the Redfish standard
- The BMC settings prefix and name have been changed along with the new architecture





# **UEFI** configuration settings through OneCLI

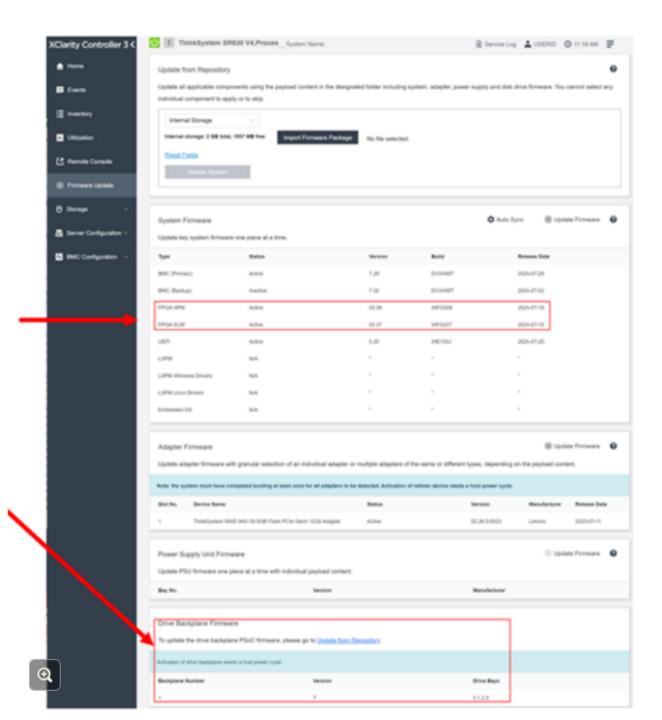
 The name of UEFI settings aligns with Redfish BIOS attribute names, and UEFI has been added as a prefix

```
enjamin@localhost onecli_5.0.0]$ ./onecli confiq show UEFT --bmc USERID:Passw0rd12@10.240.218.180
                                                                                                                 njamin@localhost onecli 5.0.0]$ ./onecli config show UEF1
                                                                                                                s|Certificate check finished [100%] [-----
ls]Certificate check finished [100%][---
                                                                                                               tart to connect SMC at 10.240.219.149 to apply config show
tart to connect BMC at 10.240.218.180 to apply config show
                                                                                                                nnected to BMC at IP address 10.240.219.149 by REDFISH
nyoking SHOW command ...
MEFI AdvancedRAS DIMMDisableFolicy=DisableFaultyDIMMPersistently
                                                                                                                ocessors.DeterminismSlider=Performance
TEFI AdvancedRAS MachineCheckRecovery=Enabled
                                     Add UEFI as prefix
MEFI AdvancedRAS PCIErrorRecovery-Enabled
                                                                                                                ocessors.CorePerformanceBoost=Enabled
                                                                                                                ocessors.cTDP-Auto
                                                                                                                ocessors.PackagePowerLimit-Auto
       wodes_InfiniteBootRetry=Disabled
                                                                                                                ocessors.4-LinkxOMIMaxSpeed-Minimum
                                                                                                                 cessors, GlobalC-stateControl=Enabled
    BootModes PreventOSChangesToBootOrder=Disabled
                                                                                                                ocessors.SOCP-states-Auto
EFI BootModes SystemBootMode=UEFIMode
                                                                                                                ocessors.DFC-States=Enabled
    BootOrder BootOrder 1=USBStorage
                                                                                                                ocessors.P-statel=Enabled
   BootOrder BootOrder 3-DVDROM
    BootOrder BootOrder 4-None
                                                                                                                  essors.CPUSpeculativeStoreModes=Balanced
   BootOrder [WDROMPriority 1=None
MEFI BootOrder HardDiskPriority 1-Wone
                                                                                                                ocessors.LiStreamNWPrefetcher-Enabled
   BootOrder NetworkPriority I-Slot8PortOPNEv4Broadcom571916bERJ454 portOCPEthernetAdapter
                                                                                                                ocessors, L2StreamHWFrefetcher=Enabled
    BootOrder NetworkPriority 2-Slot0Port0PXXv6Broadcom57191GbERJ454 port0CPEthernetAdapter
                                                                                                                cessors.LiStrideFrefetcher=Enabled
    BootOrder NetworkFriority 3-51ot0Port1PXEv4Broadcom571910bERJ454 portOCPEthernetAdapter
                                                                                                                ocessors.LiRegionPrefetcher-Enabled
    BootOrder NetworkPriority 4~Slot@Port1PXEv6Broadcom57191@bERJ454 portOCPEthernetAdapter
                                                                                                                ocessors.L2UpDownFrefetcher=Enabled
    BootOrder NetworkFriority 5~SlotSPort2PXEv4Broadcom57191GbERJ454 portOCPEthernetAdapter
                                                                                                                cossors.SMTMode-Enabled
   BootOrder SetworkFriority 6-SiotNPort2PXXV6Broadcom57191GbBRJ454 portOCFEthernetAdanter
   BootOrder NetworkPriority 7~Slot0Port3PNZv4Broadcom57191GbERJ454 portOCPEthernetAdapter
                                                                                                                 cessors.BoostFmax-Auto
   BootOrder NetworkFriority %-Slot@Port3PXEv6Broadcom57191GkERJ454 portOCPEthernetAdapter
                                                                                                                 cessors.SVMMode=Enabled
                                                                                                                  essors.xGMIMaximumLinkWidth~Auto
   BroadcomNetXtremeGigabitEthermetAdapter_Slot@PhysicalPortlLogicalPortl_BannerMessageTimeout=15
   BroadcomMetXtremeGigabitEthermetAdapter Slot@PhysicalPortlLogicalPortl BlinkLEDs=0
                                                                                                                ocessors.SEV-5NPSupport=Disabled
   BroadcomNetXtremeGigabitEthermetAdapter Slot8PhysicalFortlLogicalPortl BootStrapType=AutoDetect
WFI BroadcomNetXtremeGigabitEthernetAdapter Slot@PhysicalPort1LogicalPort1 Bootcode=1.47
                                                                                                                ocessors.EnhancedREFMOVSBSTOSB-Enabled
    BroadcomNetXtremeGigabitEthernetAdapter_Slot(PhysicalPortlLogicalPortl_CCM-N/A
                                                                                                                ocessors.FastShortRESMOVSB-Enabled
    BroadcomNetXtremeGigabitEthernetAdapter_Slot8PhysicalPort1LogicalPort1_EFI=21.6.36
                                                                                                                ocessors.SNPMemoryRMPTableCoverage=Disabled
   BroadcomNetXtremeGigabitEthermetAdapter Slot8PhysicalFortILogicalFort1 FirmwareBundle-222.0.2.1
                                                                                                                ocessors.xGMIForceLinkWidth=Auto
   BroadcomNetXtremeGigabitEthermetAdapter Slot8PhysicalPortlLogicalPortl LegacyBootProtocolQ3302*FXE
                                                                                                                ocessors.NumberofEnabledCFUCoresFerSocket-All
   BroadcomNetXtremeGigabitEthermetAdapter Slot@PhysicalPortlLogicalPortl LinkSpeed=AutoNeg
                                                                                                               ystemRecovery.POSTWatchdogTimer=Disabled
EFI BroadcomNetXtremeGigabitEthermetAdapter Slot8PhysicalPortILogicalPort1 LinkStatus-Disconnected
                                                                                                                stemRecovery.POSTWatchdogTimerValue=5
    BroadcomNetXtremeGigabitEthermetAdapter_Slot@PhysicalPortILogicalPort1 MBA=21.6.2
                                                                                                               ystemRecovery.RebootSystemOnMMI=Enabled
    BroadcomNetXtremeGigabitEthermetAdapter Slot8PhysicalPortlLogicalPort1 NC SI=1.5.35
                                                                                                               rstemRecovery.PostLoadSetupDefault=Disabled
          cmNetXtremeGigabitEthermetAdapter__Slot8PhysicalPort1LogicalPort1_PermanentMACAddrens=D4:04:86:80:99:EH
                                                                                                               stemRecovery.FIStartControl=Auto
          omNetXtremeGigabitEthermetAdapter Slot@PhysicalPortILogicalPortI PortEnablement~EnableAllPorts
                                                                                                               evicesandIOPorts.ActiveVideo=Onboard Device
          mNetXtremeGigabitEthermetAdapter Slot8PhysicalFortlLogicalFortl Fre bootWakeOnLAN-Enabled
                                                                                                               evicesandIOForts.PCI64BitResourceAllocation=Auto
      oadcomNetXtremeGigabitEthermetAdapter SlotSPhysicalFortiLogicalFort1 VLANID14094*1
                                                                                                                vicesandIOPorts.IOMWU-Enabled
```



#### Firmware updates

- The FPGA firmware and backplane PSoC firmware are separated from the XCC3 package
  - In previous generations, they were packaged together
- The SCM FPGA and HPM FPGA have individual firmware inventory entries, but they can be updated together
  - If FPGA firmware is updated when system power is on, a BMC reset and system reboot is needed
- The drive backplane PSoC firmware has a separate firmware inventory entry – there will be a bundle which includes all the drive backplane firmware
  - If drive backplane firmware is updated when system power is on, a system reboot is needed





# Security

- There are plans to acquire FIPS 140-3 validation and certification for XCC3
- PFR (Platform Firmware Resilience) is supported in the same way as with the ThinkSystem V3 platform
- Security mode design is the same as with the ThinkSystem V3 platform
  - Three modes are supported: Enterprise Strict, Standard, and Compatibility
- Secure out of box
  - Similar to the ThinkSystem V3 platform, non-secure protocol/functions for example,
     IPMI over LAN are disabled by default

#### Miscellaneous new functions

- In-band (IB) IPMI access is supported through KCS (Keyboard Controller Style) but not USB
   LAN
  - Out-of-band (OOB) IPMI access is supported through Ethernet interfaces
- The default IB USB LAN IPv4 address has changed from 169.254.95.118 to 172.20.95.118
- Minor changes to user interfaces will be documented in the XCC3 user guide
- Generic NIC support will be available in wave 3
- CXL memory OOB management will be supported in wave 3

